

Key Features

- 4 x SFP+ channels at 10 Gbps each
- 1 x QSFP+ channel at 40 Gbps
- PCIe Gen3 x8 Half-length card
- Up to 144 Gb of DDR3 memory
- On-board 16 Gb DDR3 64bit wide
- DDR3 SODDIMM of up to 128 Gb
- Flexible machine I/O:
 - 4 TTL configurable I/Os
 - 4 LVCMOS configurable I/Os
 - 2 LVDS inputs
 - 2 LVDS outputs
 - 4 opto-isolated outputs
 - 4 opto-isolated inputs
 - 2 quadrature rotary encoders
 - Integrated strobe controller
- Transfer Rate of up to 50 Gbps through PCIe
- Transfer Rate of up to 80 Gbps through optical interfaces
- CWDM support
- QSFP+ can be expanded to 4x 10G interfaces
- Authentication device for design security
- Temperature control
- Fan control
- 4 indication LEDs
- 0°C to 50°C operating environment temperature

Komodo Fiber Frame Grabber

Innovative Approach

Komodo-Fiber is high-performance yet low-cost FPGA card supporting four SFP+ 10GigE transceivers and a single QSFP+ 40GigE optical interface. The card is based on powerful FPGA, a flexible DDR3 memory system with up to 144 Gb of memory and 128 Gbps throughput. A high speed 8 lane Gen 3.0 PCI express interface allows fast data transfers between optical links and computer memory while a versatile GPIO with multi-standard support enables connection to external devices. The QSFP+ and SFP+ interfaces are connected directly to FPGA device transceiver channels to minimize latency.

Intelligent Design

All of these features combine make the Komodo-Fiber ideal for a wide range of applications, including network processing and security, compute and storage, instrumentation, broadcast, defense and aerospace.

Datasheet | Komodo™ Fiber Frame Grabber



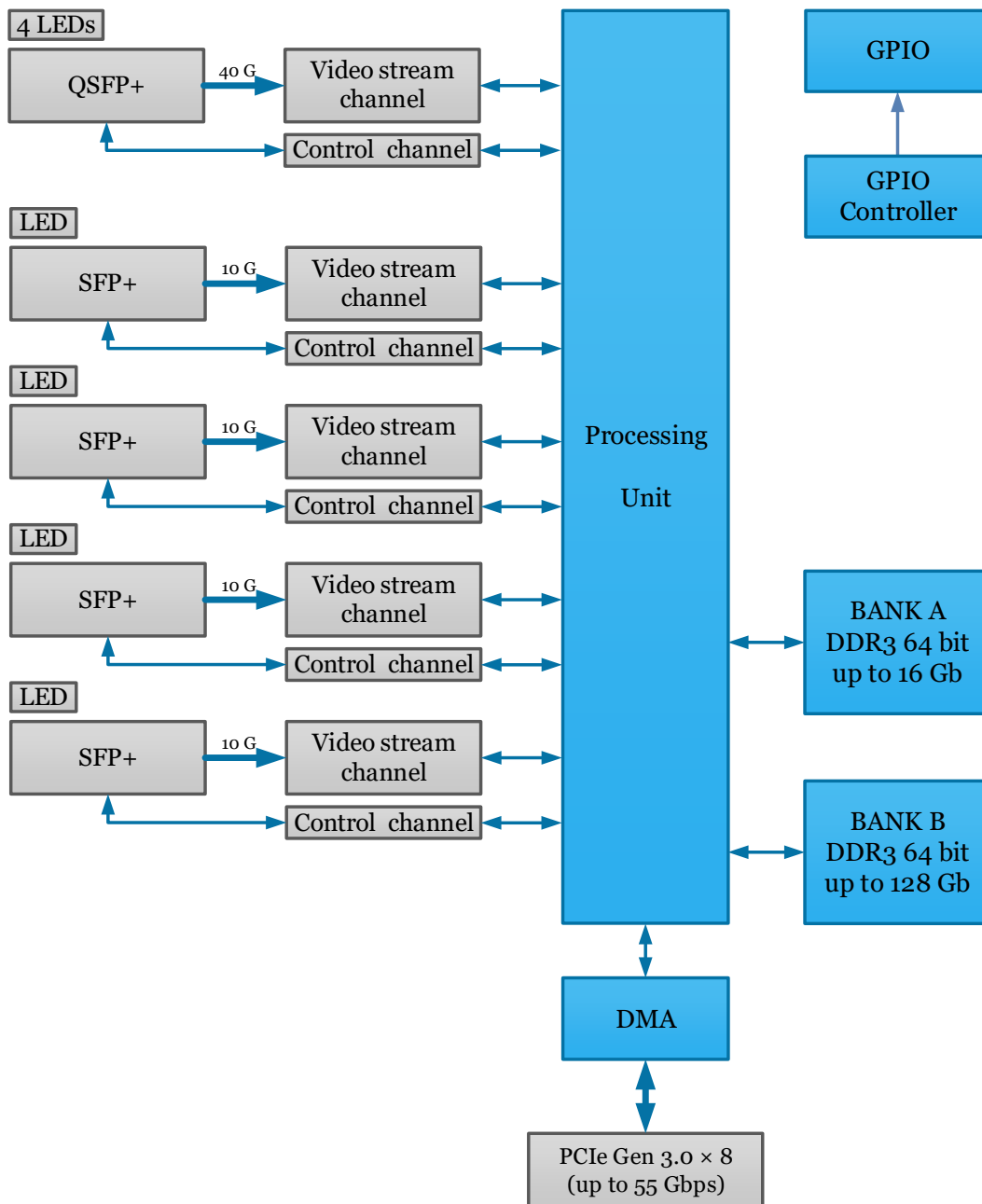
Product Name	Komodo Fiber Frame Grabber
Form Factor	PCI Express card
Format	Standard profile, half length, 8-lane PCI Express card
Cooling method	Air cooling, fan-cooled heatsink (Optional passive bracket)
Mounting	For insertion in a standard height, 8-lane or higher, PCI Express card slot
Connectors	4x SFP+ connectors 1x QSFP+ connector Internal I/O connector (with expansion bracket) 26-pin 2-row 0.1" pitch pin header with shrouding for I/O lines
Dimensions	L 167.65 mm x H 111.15 mm L 6.6 in x H 4.38 in
Weight	225gr
Hot bus	
Standard	PCI Express 3.0
Link width	8 lanes, 1, 2 or 4 lanes with reduced performance
Link speed	<ul style="list-style-type: none"> • 8.0 GT/s (PCIe 3.0) • 5.0 GT/s (PCIe 2.0) with reduced performance
Maximum payload size	512 bytes
DMA	<ul style="list-style-type: none"> • 32- and 64-bit • Scatter gather support • Physical address support (GPU transfers)
Peak delivery bandwidth	7,880 MB/s
Effective (sustained), delivery bandwidth	6,710 MB/s (Host PC motherboard dependent)
Power consumption	Typ. 16.8 W (3.8 W @ +3.3V, 13 W @ +12V), excluding camera and I/O power output
Camera / video inputs	
Interface standard(s)	KAYA Vision interface (Optional CLHS, 10GiGE Vision)
Status LEDs	1 Host connection status per connector 4 System status LEDs
Number of cameras	Up to 8
Number of links, per single camera	Up to 8
Synchronization between cameras	Yes
Line-scan cameras supported	Yes

Maximum aggregated camera data transfer rate	50 Gbit/s
Camera types	<p>Area-scan cameras:</p> <ul style="list-style-type: none"> • Gray-scale and color (RGB and Bayer CFA) • Single-tap (1X-1Y) progressive-scan <p>Line-scan cameras:</p> <ul style="list-style-type: none"> • Gray-scale and color RGB
Camera pixel formats supported	<p>Raw, Monochrome, Bayer, RGB, YUV, YCbCr and RGBA (PFNC names):</p> <ul style="list-style-type: none"> • Raw • Mono8, Mono10, Mono12, Mono14, Mono16 • BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG • RGB8, RGB10, RGB12, RGB14, RGB16 • YUV411_8, YUV411_10, YUV411_12, YUV411_14, YUV411_16 • YUV422_8, YUV422_10, YUV422_12, YUV422_14, YUV422_16 • YUV444_8, YUV444_10, YUV444_12, YUV444_14, YUV444_16 • YCbCr601_411_8, YCbCr601_411_10, YCbCr601_411_12, YCbCr601_411_14, YCbCr601_411_16 • YCbCr601_422_8, YCbCr601_422_10, YCbCr601_422_12, YCbCr601_422_14, YCbCr601_422_16 • YCbCr601_444_8, YCbCr601_444_10, YCbCr601_444_12, YCbCr601_444_14, YCbCr601_444_16
Area-scan camera control	
Trigger	<p>Precise control of asynchronous reset cameras, with exposure control.</p> <p>Support of camera exposure/readout overlap.</p> <p>Support of triggering from encoder or timer.</p> <p>Support of external hardware trigger, with optional delay, filtering and trigger decimation.</p>
Strobe	<p>Accurate control of the strobe position for strobe light sources.</p> <p>Support of early and late strobe pulses.</p>
Line-scan camera control	
Scan/page trigger	<p>Precise control of start-of-scan and end-of-scan triggers.</p> <p>Support of external hardware trigger, with optional delay and filtering.</p> <p>Support of triggering from encoder.</p> <p>Support of infinite acquisition, without missing lines.</p>
Line trigger	<p>Support for quadrature motion encoders, with programmable filters, selection of acquisition direction and backward motion compensation.</p>
Line strobe	<p>Accurate control of the strobe position for strobe light sources.</p>
On-board processing	
On-board memory	<ul style="list-style-type: none"> • 2 GB • Up to 16GByte SODIMM
Bayer De-Mosaic	<p>Full 16bit resolution</p> <ul style="list-style-type: none"> • Bilinear 3x3 • Bilinear 3x2 for linescan with gradient correction
Color Transformation	<p>Full 16bit resolution 18bit coefficients table</p> <ul style="list-style-type: none"> • Color space conversion • Gain and Offset
Decimation	Line skip
Additional features	Unpacking of 10-/12-/14-bit to 16-bit with justification to LSB
Frame Timestamp	64bit with 8ns precision

Data stream statistics	Measurement of: <ul style="list-style-type: none"> • Frame/Line rate • CRC Errors • Dropped frames • Received packets • Test packets
Event signaling and counting	The application software can be notified of the occurrence of various events: <ul style="list-style-type: none"> • Newly acquired buffers • Camera and Illumination control events • I/O events • Timer events • Encoder events
General Purpose Inputs and Outputs	
Number of lines	20 I/O lines: <ul style="list-style-type: none"> 2 differential inputs 2 differential outputs 4 singled-ended TTL inputs/outputs 4 singled-ended LVCTTL inputs/outputs 4 opto-isolated inputs 4 opto-isolated outputs
Usage	Any System I/O input lines can be connected to any I/O line Any I/O line can be used to decode A/B and Z signals of a motion encoder Any I/O line can generate any trigger event Any I/O line can trigger a timer
Electrical specifications	Differential lines - LVDS compatible TTL lines - 5V TTL compliant LVTTTL lines - 3.3V LVTTTL compliant Isolated lines - opto isolated lines with voltage range up to 30V
Filter control	Glitch removal filter available on all System I/O input lines Configurable filter time constants for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s
Polarity control	Yes
Encoders	4 quadrature encoders with A/B and Z inputs 32bit position counter Forward and backward counting Position trigger support Noise filtering
Timers	8 general purpose timers Configurable delay and duration 32bit accumulator
Event reporting	64bit system timestamp event reporting Each I/O line can generate event on configurable edge Each Timer can generate event Each encoder can generate event

Frame grabber synchronization	
Synchronization	Precise area and linescan cameras synchronization across different frame grabbers
Software	
Host PC Operating System	Microsoft Windows 7/10 32- and 64-bit versions, Linux open source driver compatible with a wide range of distributions, tested and precompiled for Ubuntu 14.04 , RedHat 6.5 , CentOS 7 32- and 64-bit versions
Buffer management	Circular buffer support Accumulation of several frames/lines to single buffer to reduce CPU load DMA Buffer filling directly to system memory
Gen<i>Cam	Support of Gen<i>Cam up to 2.4 Full camera and frame grabber parameters configuration
GUI	Supported for Windows and Linux OS Multicamera display and configuration Flexible buffer queuing Image/video recording and playback
Debugging capabilities	Event logging Statistics counters
Environmental conditions	
Operating ambient air temperature	0°C to +50°C / +32°F to +122 °F
Operating ambient air humidity	10% to 90% RH non-condensing
Storage ambient air temperature	-20°C to +70°C / -4°F to +158°F
Storage ambient air humidity	10% to 90% RH non-condensing
Certifications	
Electromagnetic - EMC standards	The European Council EMC Directive 2004/108/EC The Unites States FCC rule 47 CFR 15
EMC - Emission	EN 55022:2010 Class B FCC 47 Part 15 Class B
EMC - Immunity	EN 55024:2010 Class B EN 61000-4-3 EN 61000-4-4 EN 61000-4-6
Flammability	PCB compliant with UL 94 V-0
RoHS	Compliant with the European Union Directive 2011/65/EU (ROHS2)
REACH	Compliant with the European Union Regulation No 1907/2006
WEEE	Must be disposed of separately from normal household waste and must be recycled according to local regulations
Ordering Information	KY-FGF
Optional accessories	<ul style="list-style-type: none"> • GPIO Expansion bracket • DDR3 Extra memory SODIMM 2GB, 4GB, 8GB or 16GB • SFP+ optical modules • QSFP+ optical modules • Fiber cables

Komodo Fiber Frame Grabber HW Block Diagram



Compatibility

Supported vision standard



Supported vision libraries



Supported operating systems



Compatible with most popular machine vision libraries

KAYA Instrument strives to create and maintain compatibility and interfaces for the most common and advanced vision image processing libraries and applications. Major support is available for **MVTec Halcon**, **National Instruments LabVIEW** and **MathWorks MATLAB**. Please check our KAYA website for an up-to-date list of other supported libraries and software package.

Contact

Get in touch with our teams at info@kayainstruments.com. We will be glad to assist and consult you regarding our products.

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<http://www.kayainstruments.com/products/frame-grabbers/>

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